



Patent  
706316-1178

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: )  
Takahide Ohkami ) Group Art Unit: 2123  
Application No.: 09/879,658 ) Examiner: Ayal I. Sharon  
Filed: June 11, 2001 ) Confirmation: 2956  
For: Hardware-Assisted Design Verification )  
System Using a Packet-Based Protocol Logic )  
Synthesized For Efficient Data Loading and )  
Unloading )

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**TRANSMITTAL LETTER**

Commissioner For Patents  
P.O. Box 1450  
Alexandria VA 22313-1450  
Mail Stop ISSUE FEE

Sir:

In response to the Notice of Allowance mailed by the United States Patent and Trademark Office on January 30, 2006, enclosed for filing in the above-identified application are the following:

**DOCUMENTS ENCLOSED:**

Fee(s) Transmittal (PTO form PTOL-85)

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CERTIFICATE OF MAILING  
(37 C.F.R. §1.8a)

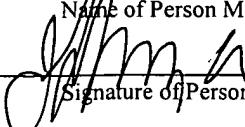
I hereby certify that this paper (along with any referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as First Class Mail in an envelope addressed to the Commissioner For Patents , P.O. Box 1450, Alexandria VA 22313-1450, Mail Stop ISSUE FEE

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706316-1207 R1H

  
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Patent  
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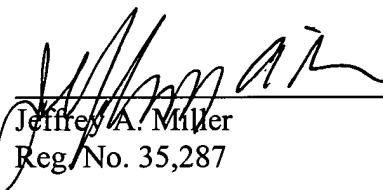
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Respectfully submitted,

Orrick, Herrington & Sutcliffe LLP

Dated: March 24, 2006

By:

  
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